



T&A-110

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. MIYAMOTO et al

Serial No. 10/046,446

Group Art Unit: 2811

Filed: January 16, 2002

Examiner: D. Owens

For: SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD OF THAT

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed November 5, 2003,
please amend the above-identified application as follows.

RECEIVED
FEB 13 2004
TECHNOLOGY CENTER 2800

Ser. No. 10/046,446

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IN THE TITLE

Amend the title to:

WAFER LEVEL CHIP SIZE PACKAGE HAVING REROUTING LAYERS